

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

4-11-02
R/N
JC525 U.S. PTO
09/664361
09/18/00



IN RE APPLICATION OF: Youichi TOBITA
SERIAL NO: New Application
FILED: Herewith
FOR: MULTILAYERED WIRING SUBSTRATE

GAU:
EXAMINER:

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

Applicant(s) wish to disclose the following information.

REFERENCES

The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed references are attached, where required, as are either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.

A check is attached in the amount required under 37 CFR §1.17(p).

RELATED CASES

Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present application. A copy of the patent(s) is attached along with PTO 1449.

A check is attached in the amount required under 37 CFR §1.17(p).

CERTIFICATION

Each item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.

No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

PETITION

Applicant(s) hereby request consideration of the attached information. A check is attached in the amount of the Petition fee required under 37 CFR §1.17(i)(1).

DEPOSIT ACCOUNT

Please charge any additional fees for the papers being filed herewith and for which no check is enclosed herewith, or credit any overpayment to deposit account number 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.



Marvin J. Spivak
Registration No. 24,913

C. Irvin McClelland
Registration Number 21,124



22850

Tel. (703) 413-3000
Fax. (703) 413-2220
(OSMMN 10/98)

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STATEMENT OF RELEVANCY

Reference AO (4-170788) on Form PTO-1449.

The following is English translation of an extract from the above-identified document relevant to the present application.

A dummy bit line 10a is wired on the edge portion of one memory array 1 provided adjacent to the other memory array 2. A dummy bit line 10b is wired parallel to said dummy bit line 10a on the edge portion of the other memory array 2, forming a dummy bit line pair 10 comprising dummy bit lines 10a and 10b.

Form PTO 1449 (Modified)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY DOCKET NO. 197211US2	SERIAL NO. New Application
LIST OF REFERENCES CITED BY APPLICANT		APPLICANT Youichi TOBITA	GROUP
		FILING DATE HEREWITH	

JC525 U.S. 654361
09/09/00 09/18/00

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES	NO
JHA	AO	4-170788	06/18/92	Japan		X
	AP					
	AQ					
	AR					
	AS					
	AT					
	AU					
	AV					

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)

	AW	
	AX	
	AY	
	AZ	

Examiner

Jere H. Mead

Date Considered

9/31/02

*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.